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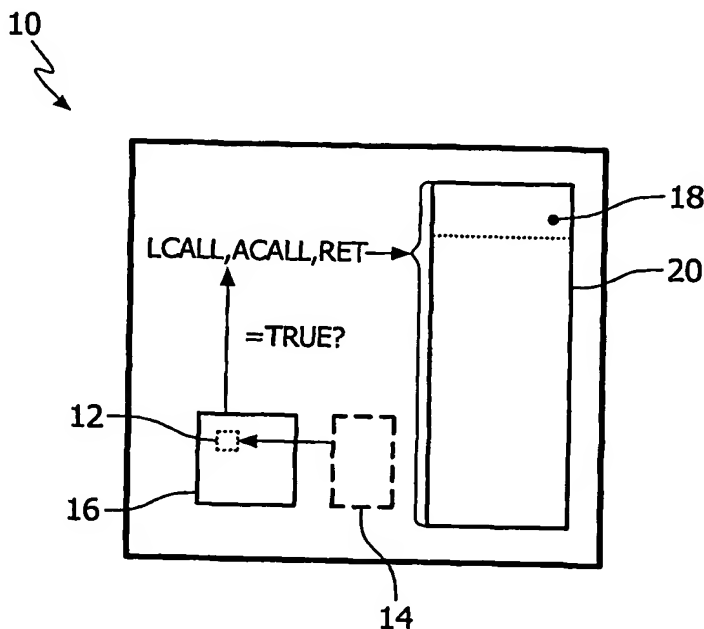
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(54) Title: MICROCONTROLLER AND ADDRESSING METHOD



(57) Abstract: In order to provide a microcontroller and an addressing method which are distinguished by a lower storage requirement and a higher execution speed than previously known when addressing N-bit address spaces, the address length N of the N-bit address word being greater than the address length of a standard set of instruction or of equivalents of other sets of instructions of the microcontroller, it is provided that the microcontroller (10) has at least one status bit (12) by means of which a writing and/or reading of N-bit address words by at least one standard instruction of the microcontroller (10) can be forced, and the at least one status bit (12) of a microcontroller (10) is set and as a result a writing and/or reading of N-bit address words by means of at least one standard instruction of the microcontroller (10) is forced.